

# DC COMPONENTS CO., LTD.

## RECTIFIER SPECIALISTS

R1200F THRU R3000F

TECHNICAL SPECIFICATIONS OF HIGH VOLTAGE FAST RECOVERY RECTIFIER

VOLTAGE RANGE - 1200 to 3000 Volts CURRENT - 0.2 to 0.5 Ampere

#### **FEATURES**

- \*Fast switching
- \*Low leakage
- \*High current capability
- \*High surge capability
- \*High reliability

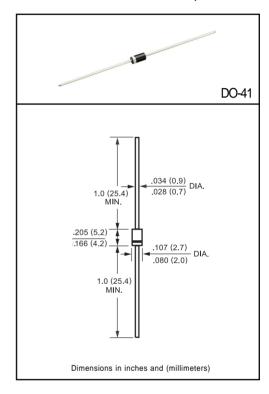
### MECHANICAL DATA

- \* Case: Molded plastic
- \* Epoxy: UL 94V-0 rate flame retardant
- \* Lead: MIL-STD-202E, Method 208 guaranteed
- \* Polarity: Color band denotes cathode end
- \* Mounting position: Any
- \* Weight: 0.35 gram

#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25  $^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%.



	SYMBOL	R1200F	R1500F	R1800F	R2000F	R2500F	R3000F	UNITS
Maximum Recurrent Peak Reverse Voltage	VRRM	1200	1500	1800	2000	2500	3000	Volts
Maximum RMS Volts	VRMS	840	1050	1260	1400	1750	2100	Volts
Maximum DC Blocking Voltage	VDC	1200	1500	1800	2000	2500	3000	Volts
Maximum Average Forward Rectified Current at TA = 50°C	lo	500 200					mAmps	
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	30					Amps	
Maximum Instantaneous Forward Voltage at 0.5A/0.2A DC	VF	2.5		4.0		5.0	Volts	
Maximum DC Reverse Current at Rated DC Blocking Voltage TA = 25°C	5.0						uAmps	
Maximum Full Load Reverse Current Average, Full Cycle .375* (9.5mm) lead length at T L = 55°C	100							uAmps
Maximum Reverse Recovery Time (Note)	trr	500						nSec
Operating and Storage Temperature Range	TJ, TSTG	-65 to + 175						٥C

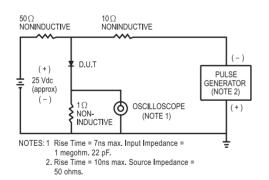
NOTES: Test Conditions: IF = 0.5A, IR = 1.0A, IRR = 0.25A

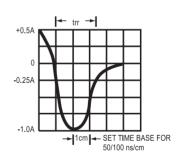
FIG. 1 - TYPICAL FORWARD CURRENT **DERATING CURVE** 500 AVERAGE FORWARD CURRENT, (A) Single Phase Half Wave 60Hz Inductive or 400 Resistive Load 300 200 100 0 0 50 100 150 175 AMBIENT TEMPERATURE, (°C)

FIG. 2 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

70
60
8.3ms Single Half Sine-Wave (JEDEC Method)
1
1
10
100
NUMBER OF CYCLES AT 60Hz

FIG. 3 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC







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